

## N-Channel Super Junction Power MOSFET IV

### General Description

The series of devices use advanced trench gate super junction technology and design to provide excellent  $R_{DS(ON)}$  with low gate charge. This super junction MOSFET fits the industry's AC-DC SMPS requirements for PFC, AC/DC power conversion, and industrial power applications.

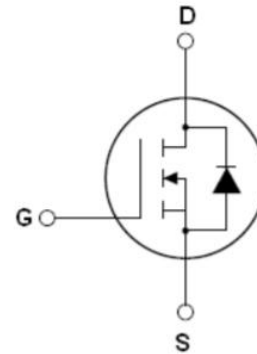
### Features

- Optimized body diode reverse recovery performance
- Low on-resistance and low conduction losses
- Small package
- Ultra Low Gate Charge cause lower driving requirements
- 100% Avalanche Tested
- ROHS compliant

### Application

- Power factor correction (PFC)
- Switched mode power supplies(SMPS)
- Uninterruptible Power Supply (UPS)
- LLC Half-bridge

$V_{DS\ min@T_{jmax}}$	710	V
$R_{DS(ON)TYP.}$	790	mΩ
$I_D$	5.1	A
$Q_g$	9	nC



Schematic diagram

### Package Marking And Ordering Information

Device	Device Package	Marking
NCE65N900I	TO-251-3L	NCE65N900I



TO-251

Table 1. Absolute Maximum Ratings ( $T_c=25^\circ\text{C}$ )

Parameter	Symbol	Value	Unit
Drain-Source Voltage ( $V_{GS}=0V$ )	$V_{DS}$	650	V
Gate-Source Voltage ( $V_{DS}=0V$ ), AC ( $f>1\text{ Hz}$ )	$V_{GS}$	$\pm 30$	V
Gate-Source Voltage ( $V_{DS}=0V$ ), DC	$V_{GS}$	$\pm 20$	V
Continuous Drain Current at $T_c=25^\circ\text{C}$	$I_{D(DC)}$	5.1	A
Continuous Drain Current at $T_c=100^\circ\text{C}$	$I_{D(DC)}$	3.57	A
Pulsed drain current (Note 1)	$I_{DM(pluse)}$	15.3	A
Maximum Power Dissipation( $T_c=25^\circ\text{C}$ )	$P_D$	59	W
Derate above $25^\circ\text{C}$		0.39	W/ $^\circ\text{C}$
Single pulse avalanche current (Note 2)	$I_{AS}$	1.5	A
Reverse diode $dv/dt$ , $V_{DS} \leq 480\text{ V}, I_{SD} < I_D$	$dv/dt$	15	V/ns
Drain Source voltage slope, $V_{DS} \leq 480\text{ V}$	$dv/dt$	50	V/ns
Operating Junction and Storage Temperature Range	$T_J, T_{STG}$	-55...+175	$^\circ\text{C}$

**Table 2. Thermal Characteristic**

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-Case (Maximum)	$R_{thJC}$	2.54	$^{\circ}C/W$
Thermal Resistance, Junction-to-Ambient (Maximum)	$R_{thJA}$	62	$^{\circ}C/W$

**Table 3. Electrical Characteristics (TA=25 $^{\circ}C$  unless otherwise noted)**

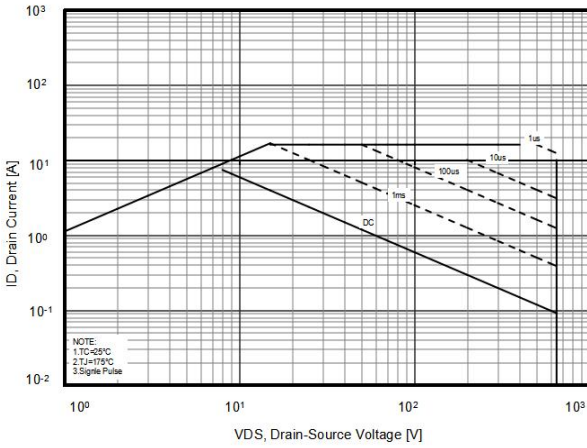
Parameter	Symbol	Condition	Min	Typ	Max	Unit
<b>On/off states</b>						
Drain-Source Breakdown Voltage	$BV_{DSS}$	$V_{GS}=0V, I_D=250\mu A$	650			V
Zero Gate Voltage Drain Current(Tc=25 $^{\circ}C$ )	$I_{DSS}$	$V_{DS}=650V, V_{GS}=0V$			1	$\mu A$
Zero Gate Voltage Drain Current(Tc=125 $^{\circ}C$ )	$I_{DSS}$	$V_{DS}=650V, V_{GS}=0V$			50	$\mu A$
Gate-Body Leakage Current	$I_{GSS}$	$V_{GS}=\pm 20V, V_{DS}=0V$			$\pm 200$	nA
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	3		4	V
Drain-Source On-State Resistance	$R_{DS(ON)}$	$V_{GS}=10V, I_D=2.5A$		790	900	m $\Omega$
<b>Dynamic Characteristics</b>						
Gate Resistance	$R_g$	F=1MHZ, D-S short		36		$\Omega$
Input Capacitance	$C_{iss}$	$V_{DS}=50V, V_{GS}=0V,$ F=1MHZ		296		pF
Output Capacitance	$C_{oss}$			12		pF
Reverse Transfer Capacitance	$C_{riss}$			4		pF
Total Gate Charge	$Q_g$	$V_{DS}=480V, I_D=2.5A,$ $V_{GS}=10V$		9		nC
Gate-Source Charge	$Q_{gs}$			2.3		nC
Gate-Drain Charge	$Q_{gd}$			3.2		nC
Gate plateau voltage	$V_{gp}$			5.7		V
<b>Switching times</b>						
Turn-on Delay Time	$t_{d(on)}$	$V_{DD}=480V, I_D=2.5A,$ $R_G=4\Omega, V_{GS}=10V$		7		nS
Turn-on Rise Time	$t_r$			3		nS
Turn-Off Delay Time	$t_{d(off)}$			50		nS
Turn-Off Fall Time	$t_f$			9		nS
<b>Source- Drain Diode Characteristics</b>						
Source-drain current(Body Diode)	$I_{SD}$	$T_c=25^{\circ}C$			5.1	A
Pulsed-Source-drain current(Body Diode)	$I_{SDM}$				15.3	A
Forward on voltage	$V_{SD}$	$T_j=25^{\circ}C, I_{SD}=5.1A, V_{GS}=0V$		0.9	1.1	V
Reverse Recovery Time	$t_{rr}$	$T_j=25^{\circ}C, I_F=2.5A,$ $di/dt=100A/\mu s$		190		nS
Reverse Recovery Charge	$Q_{rr}$			0.57		$\mu C$
Peak reverse recovery current	$I_{rrm}$			6		A

Notes: 1. Repetitive Rating: Pulse width limited by maximum junction temperature

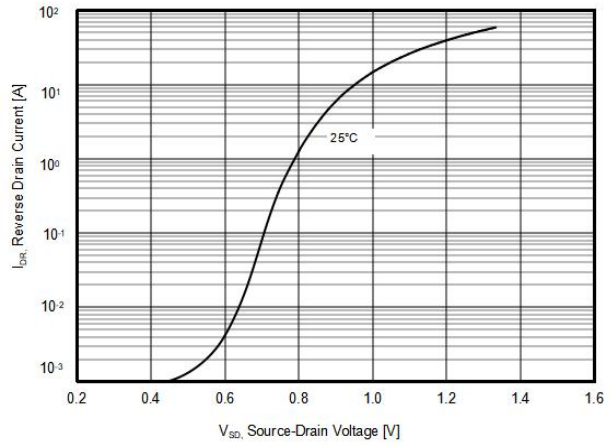
2.  $T_j=25^{\circ}C, V_{DD}=50V, V_G=10V, R_G=25\Omega$

## TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS (curves)

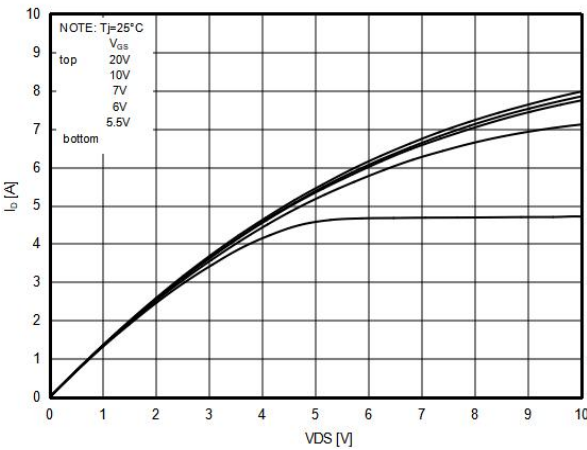
**Figure1. Safe operating area**



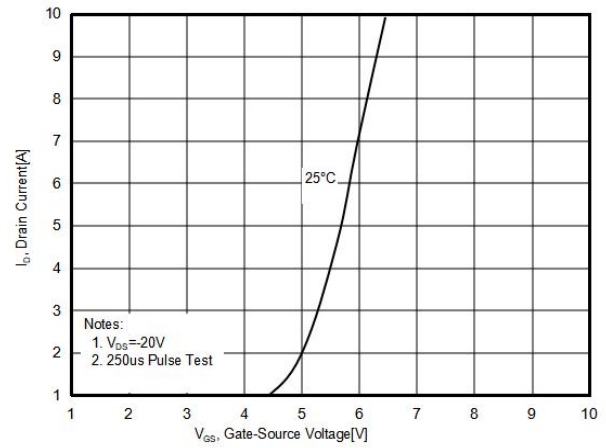
**Figure2. Source-Drain Diode Forward Voltage**



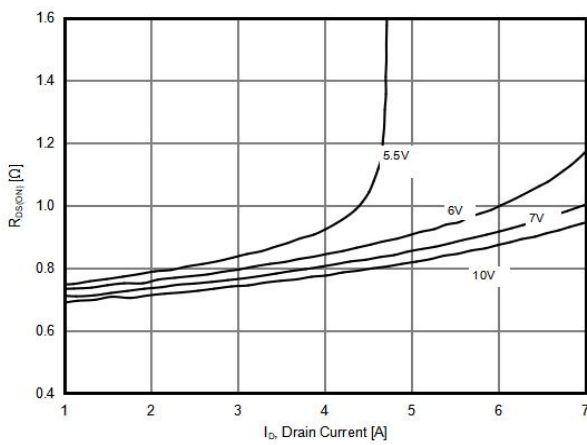
**Figure3. Output characteristics**



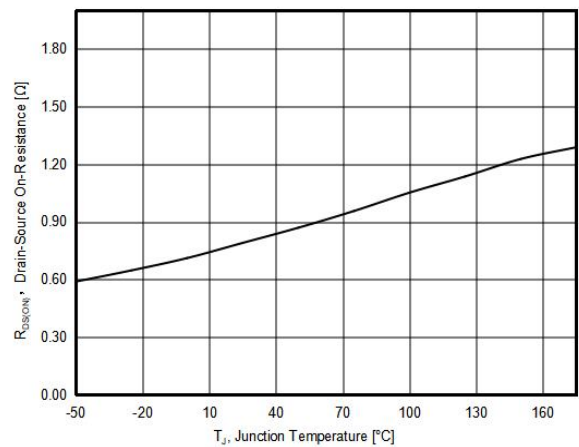
**Figure4. Transfer characteristics**



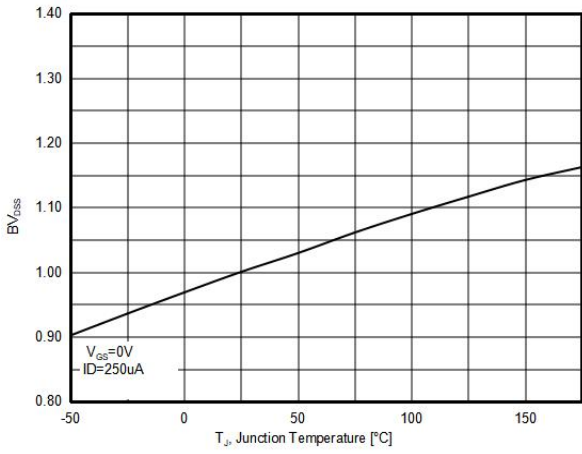
**Figure5. Static drain-source on resistance**



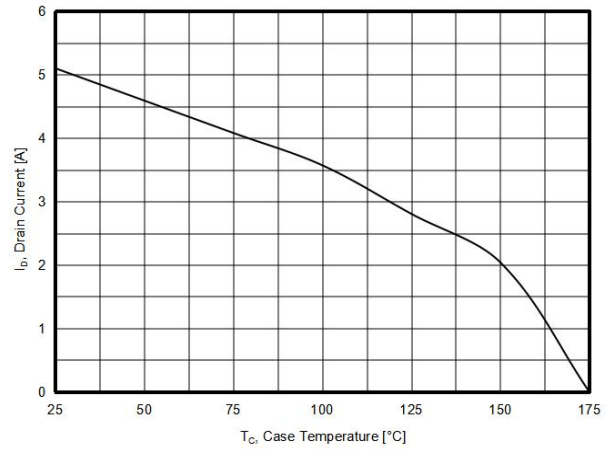
**Figure6. RDS(ON) vs Junction Temperature**



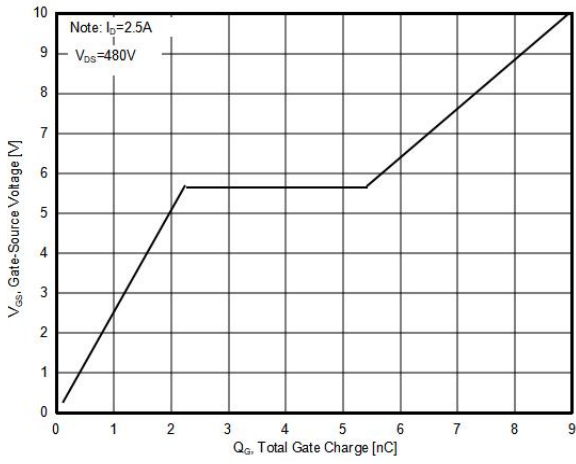
**Figure7.  $BV_{DSS}$  vs Junction Temperature**



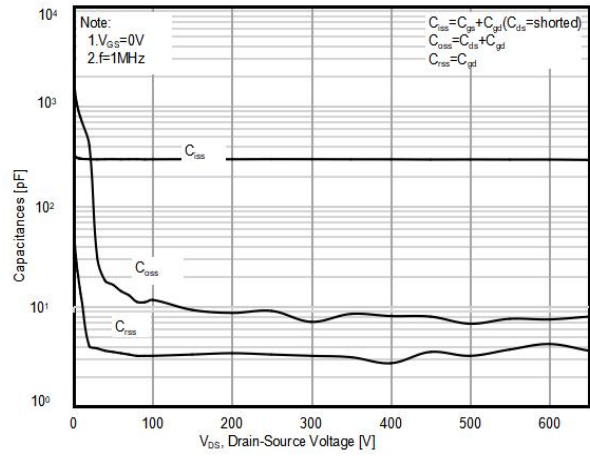
**Figure8. Maximum  $I_D$  vs Junction Temperature**



**Figure9. Gate charge waveforms**



**Figure10. Capacitance**

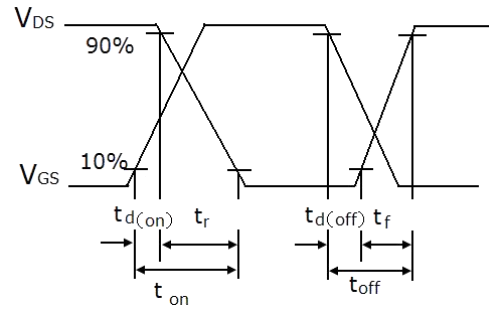


## Test circuit

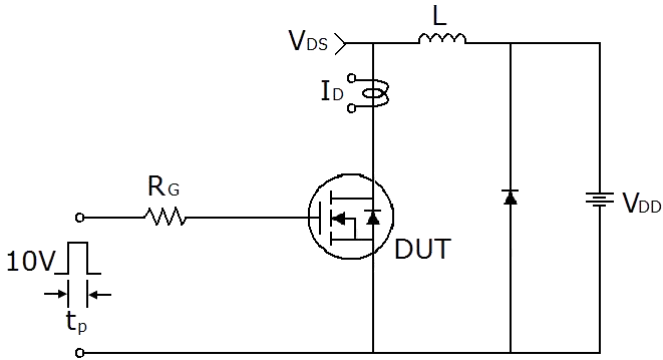
### 1) Gate charge test circuit & Waveform



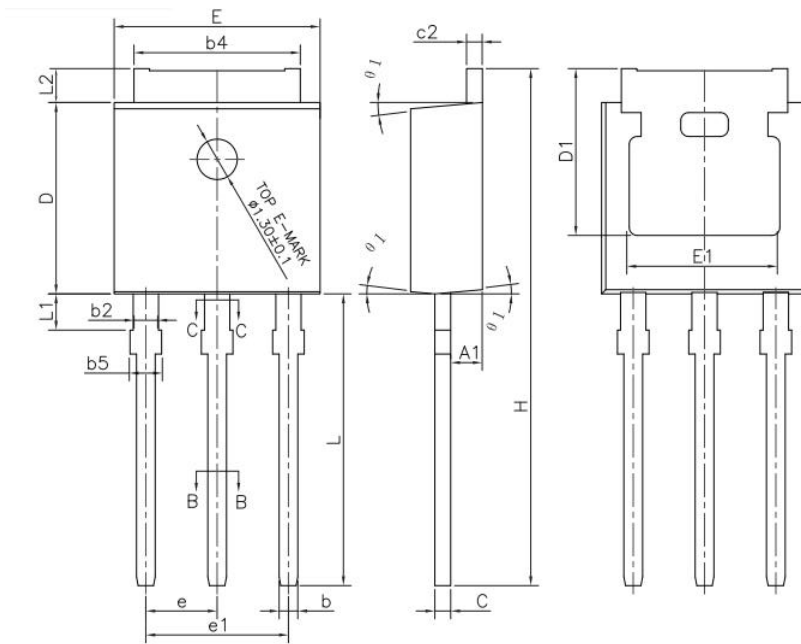
### 2) Switch Time Test Circuit:



### 3) Unclamped Inductive Switching Test Circuit & Waveforms



# TO-251-3L-P Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	2.20	2.35	0.087	0.093
A1	0.90	1.10	0.035	0.043
b	0.56	0.69	0.022	0.027
b1	0.55	0.65	0.022	0.026
b2	0.77	0.90	0.030	0.035
b3	0.76	0.86	0.030	0.034
b4	5.23	5.43	0.206	0.214
b5		1.05		0.041
c	0.46	0.59	0.018	0.023
c1	0.45	0.55	0.018	0.022
c2	0.46	0.59	0.018	0.023
D	6.00	6.20	0.236	0.244
D1	5.20		0.205	
E	6.50	6.70	0.256	0.264
E1	4.60	5.00	0.181	0.197
e	2.24	2.34	0.088	0.092
e1	4.47	4.67	0.176	0.184
H	16.18	16.78	0.637	0.661
L	9.00	9.60	0.354	0.378
L1	0.95	1.35	0.037	0.053
L2	0.90	1.25	0.035	0.049

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