

N-Channel Super Junction Power MOSFET IV

General Description

The series of devices use advanced trench gate super junction technology and design to provide excellent $R_{DS(ON)}$ with low gate charge. This super junction MOSFET fits the industry's AC-DC SMPS requirements for PFC, AC/DC power conversion, and industrial power applications.

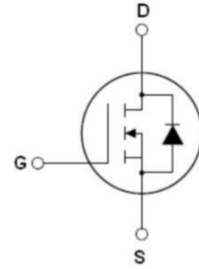
Features

- New technology for high voltage device
- Low on-resistance and low conduction losses
- Small package
- Ultra Low Gate Charge cause lower driving requirements
- 100% Avalanche Tested
- ROHS compliant

Application

- Power factor correction (PFC)
- Switched mode power supplies(SMPS)
- Uninterruptible Power Supply (UPS)

$V_{DS\ min@T_{jmax}}$	550	V
$R_{DS(ON)TYP.}$	280	m Ω
I_D	10	A
Q_g	12.5	nC



Schematic diagram

✧ **Intrinsic fast-recovery body diode**

Package Marking And Ordering Information

Device	Device Package	Marking
NCE50NF330	TO-220-3L	NCE50NF330



G D S TO-220

Table 1. Absolute Maximum Ratings ($T_J=25^\circ\text{C}$)

Parameter	Symbol	Value	Unit
Drain-Source Voltage ($V_{GS}=0V$)	V_{DS}	500	V
Gate-Source Voltage ($V_{DS}=0V$), AC ($f>1\text{ Hz}$)	V_{GS}	± 30	V
Gate-Source Voltage ($V_{DS}=0V$), DC	V_{GS}	± 20	V
Continuous Drain Current at $T_c=25^\circ\text{C}$	$I_{D(DC)}$	10	A
Continuous Drain Current at $T_c=100^\circ\text{C}$	$I_{D(DC)}$	7	A
Pulsed drain current (Note 1)	$I_{DM(pluse)}$	30	A
Maximum Power Dissipation($T_c=25^\circ\text{C}$)	P_D	93	W
Derate above 25°C		0.62	W/ $^\circ\text{C}$
Single pulse avalanche current (Note 2)	I_{AS}	2.5	A
Reverse diode dv/dt , $V_{DS} \leq 480\text{ V}, I_{SD} < I_D$	dv/dt	15	V/ns
Drain Source voltage slope, $V_{DS} \leq 480\text{ V}$	dv/dt	50	V/ns
Operating Junction and Storage Temperature Range	T_J, T_{STG}	-55...+175	$^\circ\text{C}$

Table 2. Thermal Characteristic

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-Case (Maximum)	R_{thJC}	1.61	$^{\circ}\text{C}/\text{W}$
Thermal Resistance, Junction-to-Ambient (Maximum)	R_{thJA}	62	$^{\circ}\text{C}/\text{W}$

Table 3. Electrical Characteristics ($T_J=25^{\circ}\text{C}$ unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
On/off states						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS}=0V, I_D=250\mu A$	500			V
Zero Gate Voltage Drain Current($T_c=25^{\circ}\text{C}$)	I_{DSS}	$V_{DS}=500V, V_{GS}=0V$			10	μA
Zero Gate Voltage Drain Current($T_c=125^{\circ}\text{C}$)	I_{DSS}	$V_{DS}=500V, V_{GS}=0V$			300	μA
Gate-Body Leakage Current	I_{GSS}	$V_{GS}=\pm 20V, V_{DS}=0V$			± 200	nA
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	3		5	V
Drain-Source On-State Resistance	$R_{DS(on)}$	$V_{GS}=10V, I_D=5A$		280	330	m Ω
Dynamic Characteristics						
Gate Resistance	R_g	F=1MHZ, D-S short		37		Ω
Input Capacitance	C_{iss}	$V_{DS}=50V, V_{GS}=0V,$ F=1MHz		545		pF
Output Capacitance	C_{oss}			36		pF
Reverse Transfer Capacitance	C_{riss}			4		pF
Total Gate Charge	Q_g	$V_{DS}=400V, I_D=5A,$ $V_{GS}=10V$		12.5	15.5	nC
Gate-Source Charge	Q_{gs}			5		nC
Gate-Drain Charge	Q_{gd}			3.5		nC
Gate plateau voltage	V_{gp}			6.5		V
Switching times						
Turn-on Delay Time	$t_{d(on)}$	$V_{DD}=380V, I_D=5A,$ $R_G=4\Omega, V_{GS}=10V$		9		nS
Turn-on Rise Time	t_r			12		nS
Turn-Off Delay Time	$t_{d(off)}$			43		nS
Turn-Off Fall Time	t_f			10		nS
Source- Drain Diode Characteristics						
Source-drain current(Body Diode)	I_{SD}	$T_c=25^{\circ}\text{C}$			10	A
Pulsed-Source-drain current(Body Diode)	I_{SDM}				30	A
Forward on voltage	V_{SD}	$T_J=25^{\circ}\text{C}, I_{SD}=10A, V_{GS}=0V$		1.0	1.2	V
Reverse Recovery Time	t_{rr}	$T_J=25^{\circ}\text{C}, I_F=5A,$ $di/dt=100A/\mu s$		135		nS
Reverse Recovery Charge	Q_{rr}			0.37		μC
Peak reverse recovery current	I_{rm}			5.5		A

Notes: 1. Repetitive Rating: Pulse width limited by maximum junction temperature

2. $T_J=25^{\circ}\text{C}, V_{DD}=50V, V_G=10V, R_G=25\Omega$

TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS (curves)

Figure1. Output characteristics

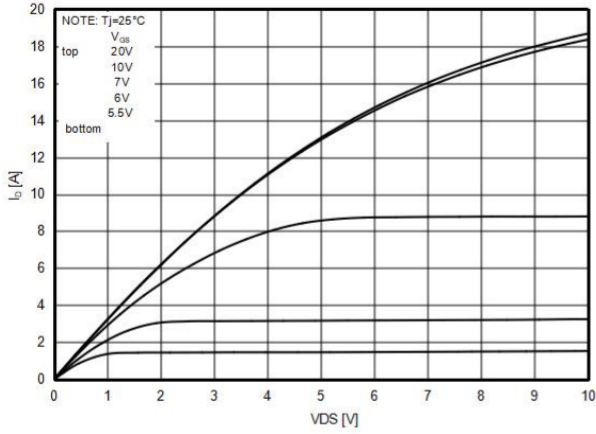


Figure2. Transfer characteristics

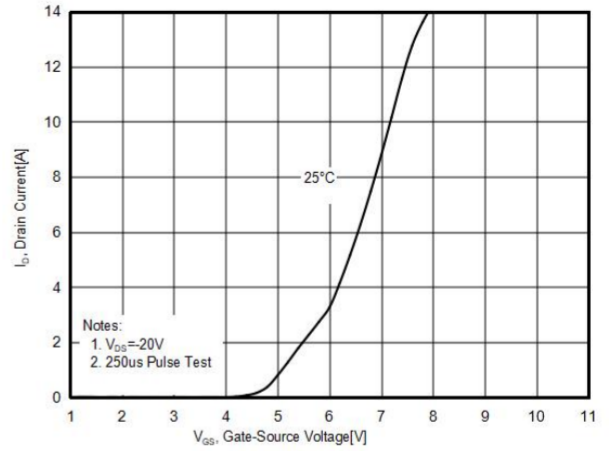


Figure3. $R_{DS(ON)}$ vs Junction Temperature

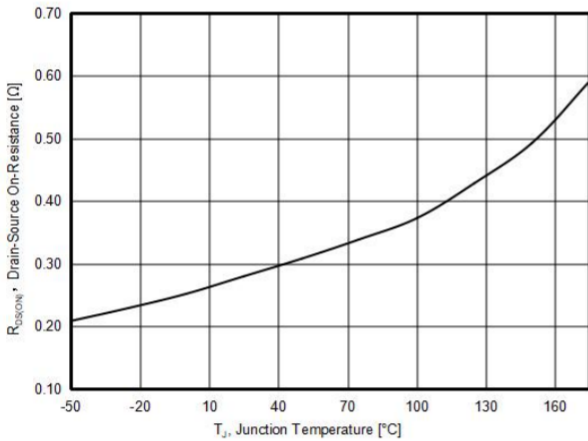


Figure4. BV_{DSS} vs Junction Temperature

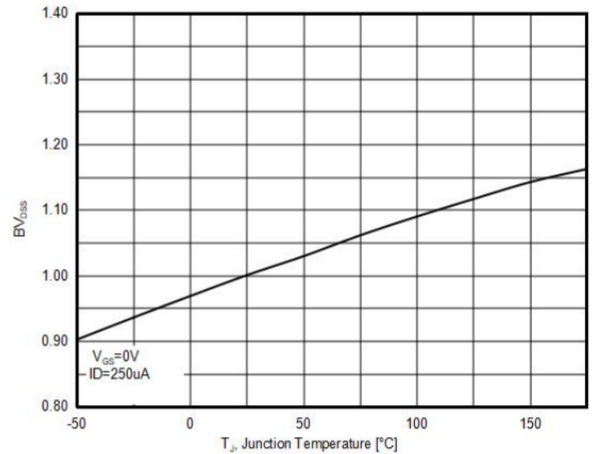


Figure5. Maximum I_D vs Junction Temperature

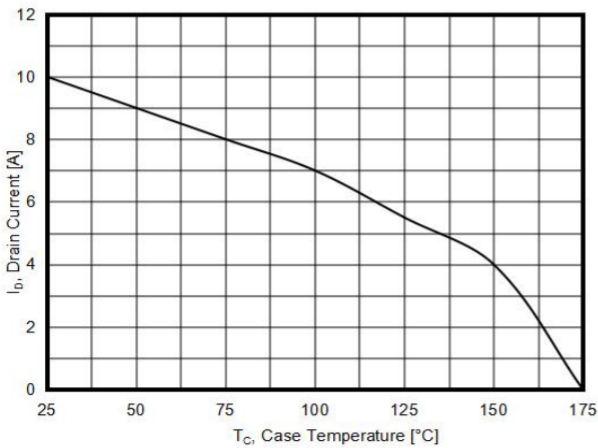


Figure6. Gate charge waveforms

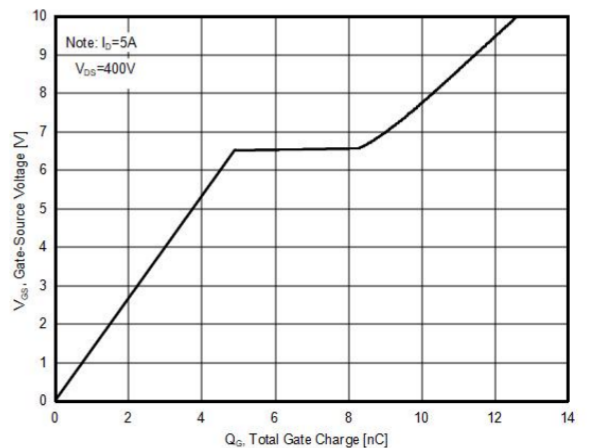


Figure7. Static drain-source on resistance

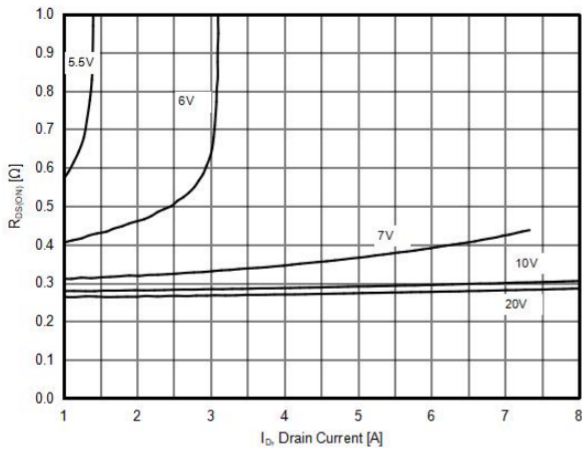


Figure8. Source-Drain Diode Forward Voltage

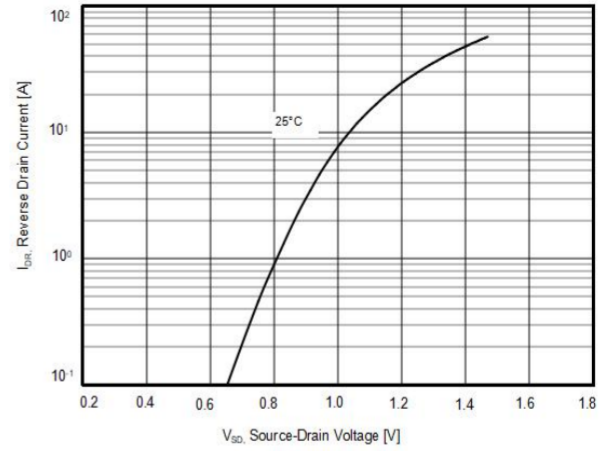


Figure9. Capacitance

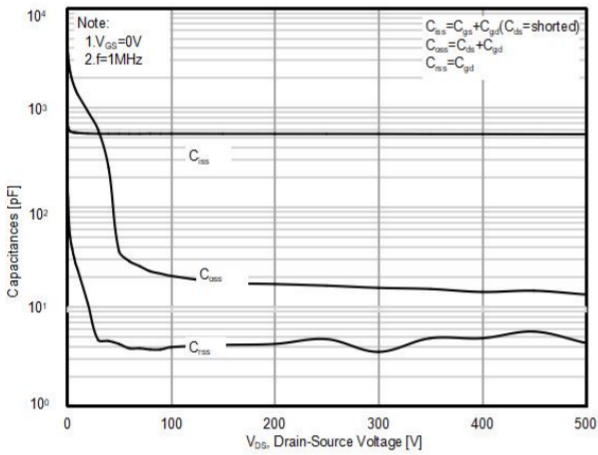
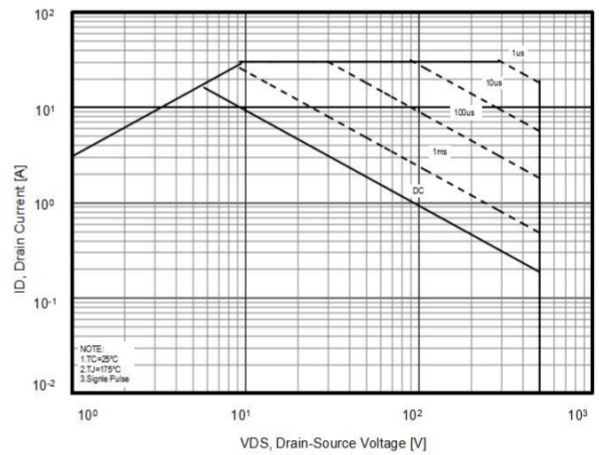
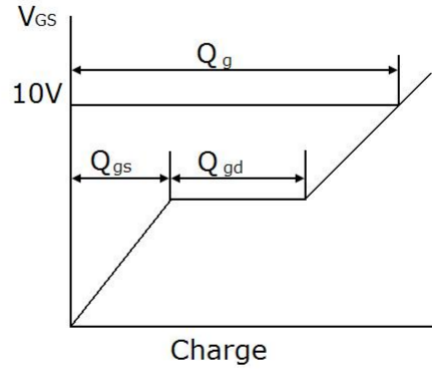
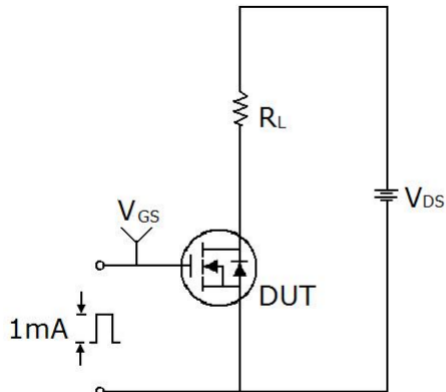


Figure10. Safe operating area

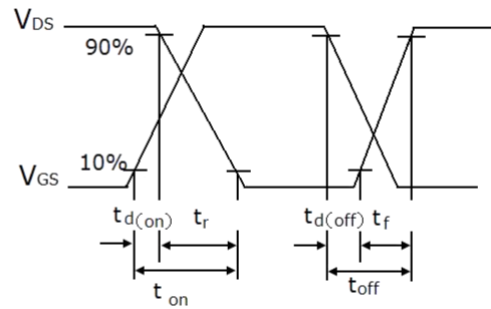
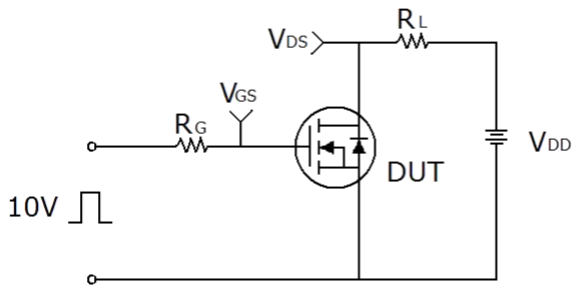


Test circuit

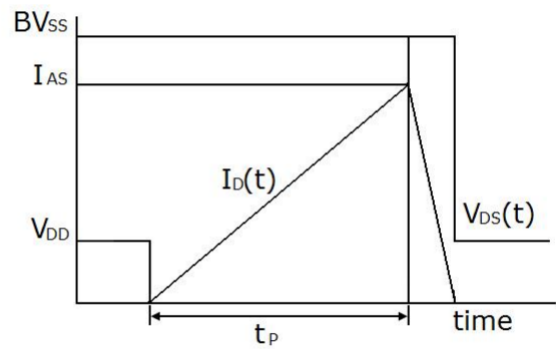
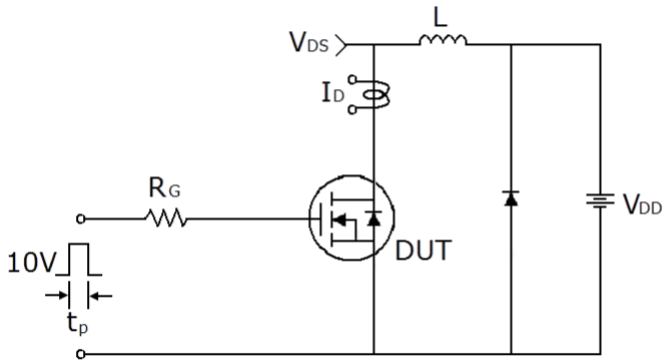
1) Gate charge test circuit & Waveform



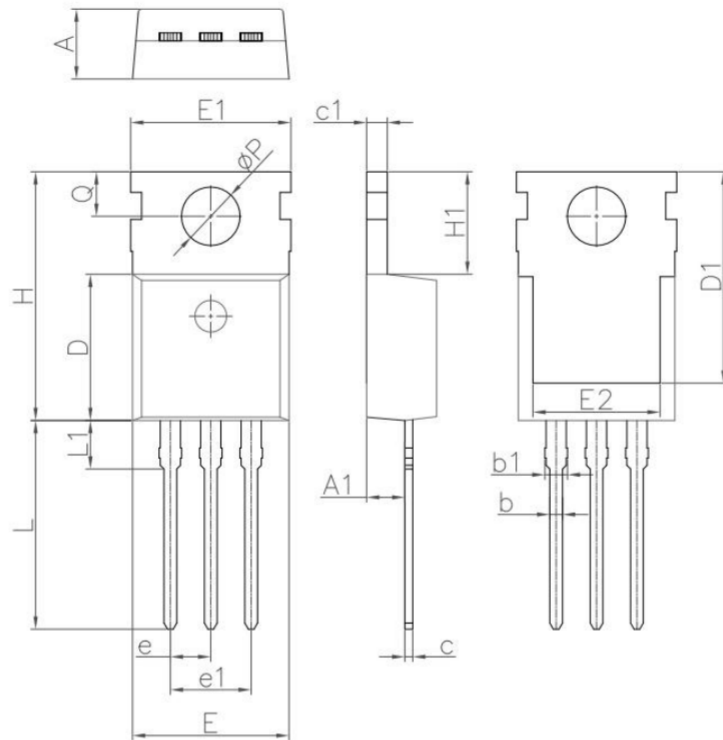
2) Switch Time Test Circuit:



3) Unclamped Inductive Switching Test Circuit & Waveforms

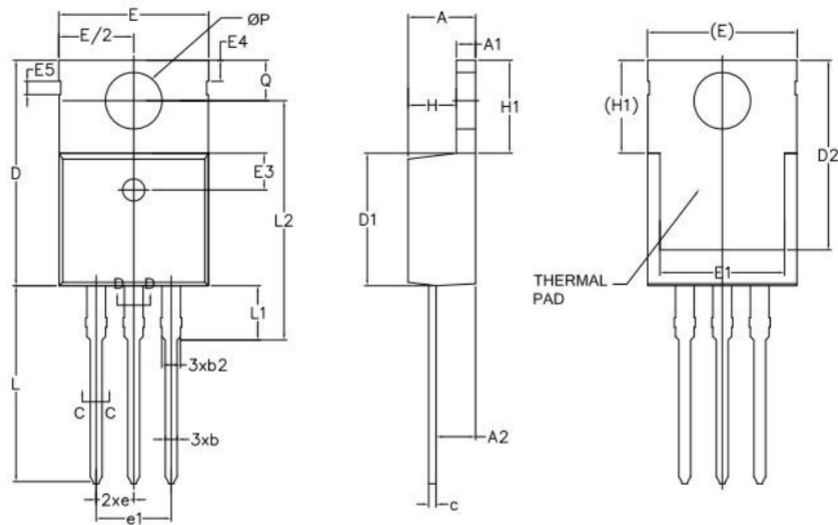


TO-220-E Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	4.20	4.60	0.165	0.181
A1	2.25	2.55	0.089	0.100
b	0.70	0.90	0.028	0.035
b1	1.17	1.37	0.046	0.054
c	0.33	0.65	0.013	0.026
c1	1.20	1.40	0.047	0.055
D	8.95	9.75	3.524	3.839
D1	13.10	13.50	5.157	5.315
E	9.74	10.04	3.835	3.953
E1	9.91	10.25	3.902	4.035
E2	7.90	8.10	3.110	3.189
e	2.54BSC		0.100BSC	
e1	5.08BSC		0.200BSC	
H	15.45	15.85	6.083	6.240
H1	6.30	6.60	2.480	2.598
L	12.90	13.40	5.079	5.276
L1	2.85	3.25	1.122	1.280
Q	2.65	2.95	1.043	1.161
ΦP	3.40	3.80	1.339	1.496

TO-220-J Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	4.47	4.67	0.176	0.184
A1	1.20	1.40	0.047	0.055
A2	2.35	2.90	0.093	0.114
b	0.71	0.91	0.028	0.036
b1	0.71	0.86	0.028	0.034
b2	1.22	1.36	0.048	0.054
c	0.47	0.60	0.019	0.024
c1	0.47	0.55	0.019	0.022
D	14.70	15.80	0.579	0.622
D1	8.90	9.47	0.350	0.373
D2	11.75	13.60	0.463	0.535
E	9.70	10.37	0.382	0.408
E1	7.00	8.89	0.276	0.350
E2	9.80	10.20	0.386	0.402
E3	2.40	2.60	0.094	0.102
E4	1.27	1.57	0.050	0.062
e	2.54BSC		0.100BSC	
e1	5.08BSC		0.200BSC	
H	3.00	3.40	0.118	0.134
L	12.90	14.80	0.508	0.583
L1	2.54	3.84	0.100	0.151
L2	12.13	16.50	0.478	0.650
ΦP	3.60	3.90	0.142	0.154
Q	2.65	2.95	0.104	0.116

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